Abstracts

A 6.5-mW receiver front-end for Bluetooth in 0.18-/spl mu/m CMOS (2002 [RFIC])

F. Beffa, R. Vogt, W. Bachtold, E. Zellweger and U. Lott. "A 6.5-mW receiver front-end for Bluetooth in 0.18-/spl mu/m CMOS (2002 [RFIC])." 2002 Radio Frequency Integrated Circuits (RFIC) Symposium 02. (2002 [RFIC]): 391-394.

This paper describes the design of a low-IF receiver front-end for Bluetooth fabricated in a 0.18 /spl mu/m CMOS technology. The chip includes an LNA and an image-reject mixer and provides a down-conversion gain to the 2 MHz IF of 21.4 dB, an image rejection of 28 dB and a NF of 13.9 dB consuming only 6.5 mW at 1.8 V.

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